

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE TENT APPLICATION

Applicant

: Seong-Hoon Lee

Application No.

: 10/722,959

Confirmation No.

: 9002

Filed

November 26, 2003

For

DIGITAL DELAY-LOCKED LOOP CIRCUITS

WITH HIERARCHICAL DELAY ADJUSTMENT

Group Art Unit

2816

New York, New York 10020

May 5, 2004

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Sir:

Pursuant to 37 C.F.R. § 1.85, applicant herewith furnishes nine (9) sheets of formal drawings to be substituted for the nine (9) sheets of informal drawings filed with this application.

Respectfully submitted,

I hereby certify that this Correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope Addressed to: Commissioner for Patents P.O. Box 14c0

Alexandria, VA 22313-1450 on

gráture of Person Signing

Evelyn C. Mak

Evelyn C. Mak Registration No. 50,492 Agent for Applicant FISH & NEAVE Customer No. 1473 1251 Avenue of the Americas New York, New York 10020-1105 (212) 596-9000

Applicant: Seong-Hoon Lee Docket No.: MIC-40 Filed: November 26, 2003 Confirmation No.: 900 Application No.: 10/722,959
For: DIGITAL DELAY-LOCKED LOOP CIRCUITS WITH HIERARCHICAL Confirmation No.: 9002

DELAY ADJUSTMENT

Agent: Evelyn C. Mak - Reg. No. 50,492

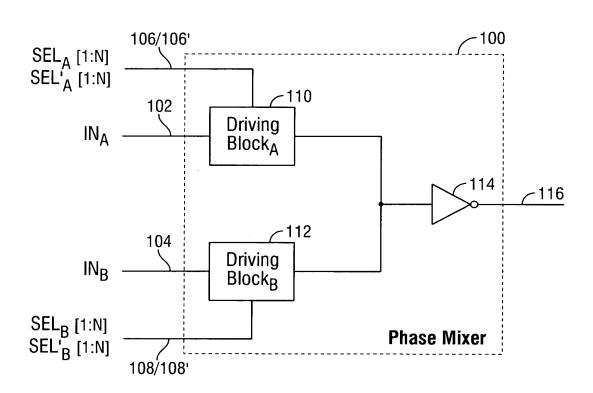


FIG. 1

Applicant: Seong-Hoon Lee Docket No.: MIC-40
Filed: November 26, 2003 Confirmation No.: 900
Application No.: 10/722,959
For: DIGITAL DELAY-LOCKED LOOP CIRCUITS WITH HIERARCHICAL Docket No.: MIC-40 Confirmation No.: 9002

DELAY ADJUSTMENT Agent: Evelyn C. Mak - Reg. No. 50,492



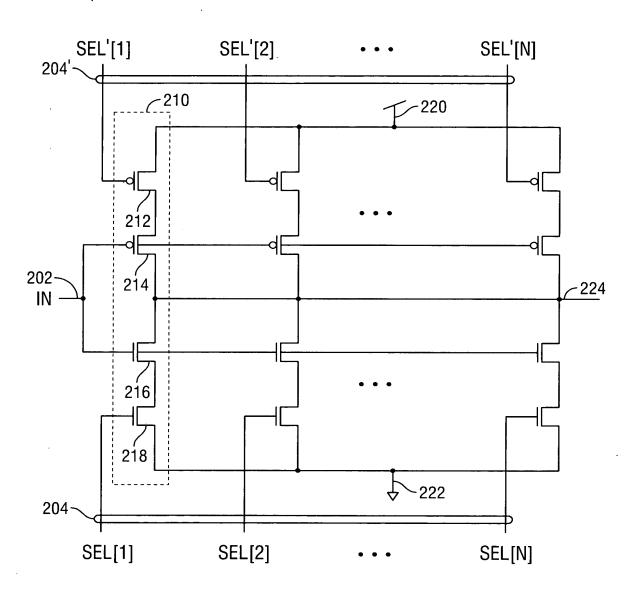


FIG. 2

Applicant: Seong-Hoon Lee Docket No.: MIC-40 Filed: November 26, 2003 Confirmation No.: 900 Application No.: 10/722,959
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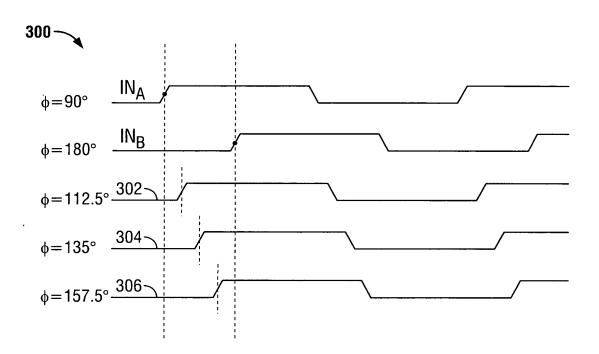


FIG. 3

Applicant: Seong-Hoon Lee Filed: November 26, 2003 Application No.: 10/722,959

Docket No.: MIC-40 Confirmation No.: 9002

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Agent: Evelyn C. Mak - Reg. No. 50,492

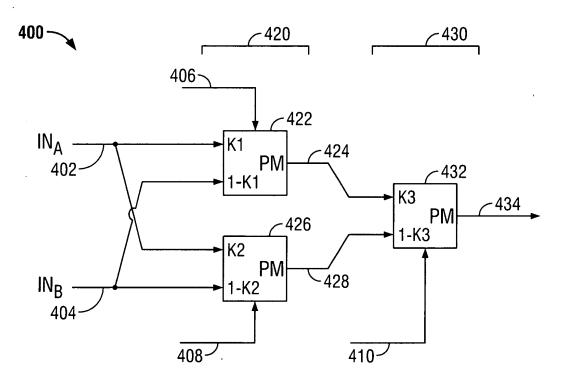


FIG. 4

Applicant: Seong-Hoon Lee Filed: November 26, 2003

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For: DIGITAL DELAY-LOCKED LOOP CIRCUITS WITH HIERARCHICAL

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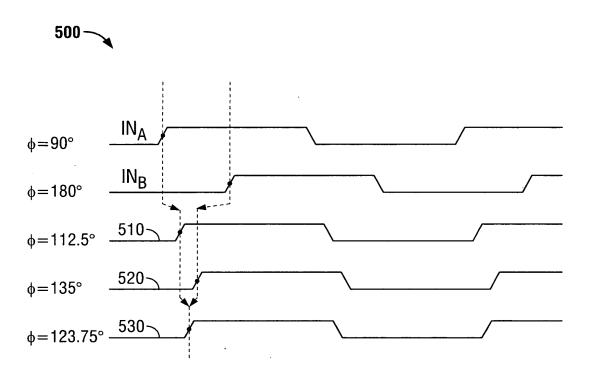
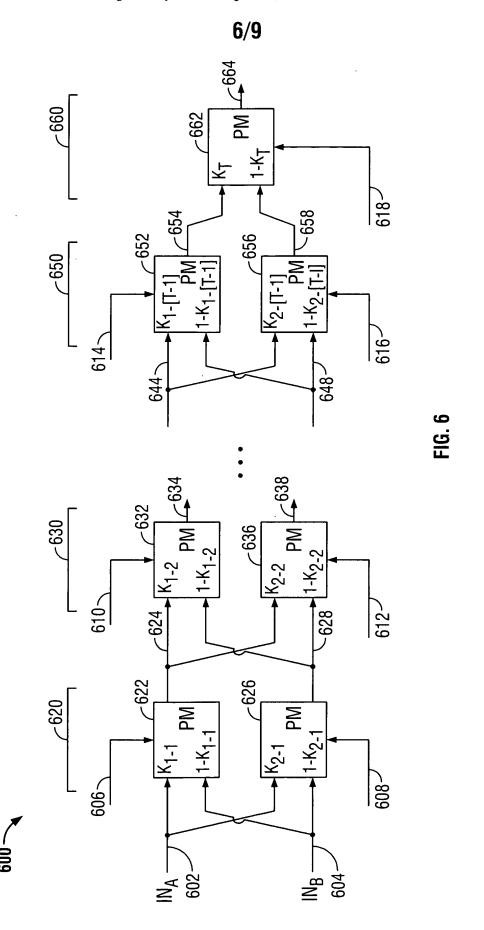


FIG. 5

Applicant: Seong-Hoon Lee Docket No.: MIC-40 Filed: November 26, 2003 Confirmation No.: 900 Application No.: 10/722,959
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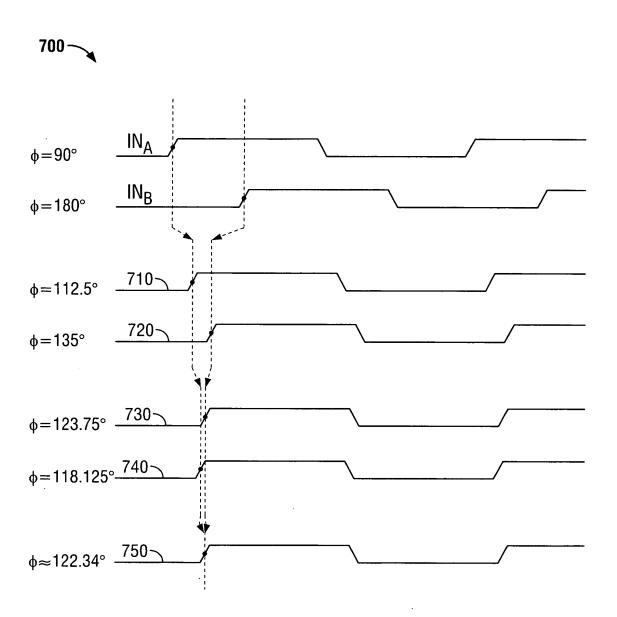
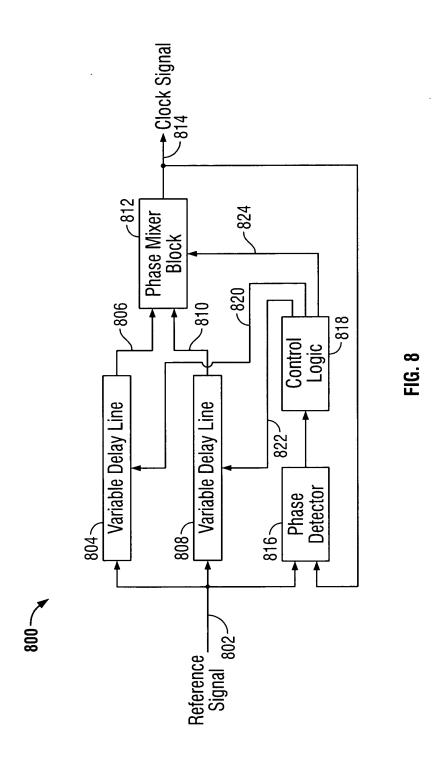


FIG. 7

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